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PATENT APPLICATION

Q-51885

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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TECHNOLOGY CENTER 2800

In re application of

Akitaka KIMURA

Appln. No.: 09/161,981

Group Art Unit: 2812

Confirmation No.: 8269

Examiner: J. Davie

Filed: September 29, 1998

For: SEMICONDUCTOR LAYER FORMED BY SELECTIVE DEPOSITION AND METHOD FOR
DEPOSITING SEMICONDUCTOR LAYER

PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. § 1.136

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.136, Applicant hereby petitions for an extension of time of one (1) month, extending the time for responding to the Office Action of May 21, 2003 to September 21, 2003.

A check for the statutory fee of \$110.00 is attached. Please charge any additional fees under 37 C.F.R. § 1.16 or § 1.17 necessary to keep this application pending in the Patent and Trademark Office or credit any overpayment to Deposit Account No. 19-4880. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

John H. Mion
Registration No. 18,879

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Date: August 26, 2003